

**Notice of References Cited**Application/Control No.  
09/404,923Applicant(s)/Patent Under  
Reexamination  
DOUEZY ET AL.Examiner  
Samarina MakhdoomArt Unit  
2123

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,304,125 ✓	10-2001	Sutherland, Ivan E.	327/141
	B	US-5,954,787 ✓	09-1999	Eun, Myung-Su	708/276
	C	US-5,491,442 ✓	02-1996	Mirov et al.	327/115
	D	US-5,838,956 ✓	11-1998	Kawasaki et al.	327/291
	E	US-6,055,489 ✓	04-2000	Beatty et al.	327/512
	F	US-6,064,247 ✓	05-2000	Krakirian, Shahe H.	327/115
	G	US-6,135,648 ✓	10-2000	Stefek et al.	703/13
	H	US-6,392,496 ✓	05-2002	Lee et al.	327/156
	I	US-4,759,014 ✓	07-1988	Decker et al.	370/503
	J	US-5,701,441 ✓	12-1997	Trimberger, Stephen M.	326/39
	K	US-5,623,418 ✓	04-1997	Rostoker et al.	716/1
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rahkonen, T.; Eksyma, H., "A 3-V programmable clock generator with a built-in phase interpolator," Proceedings of the 1998 Midwest Symposium on Circuits and Systems, 1998. Pages: 488 -491. ✓
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.